

**TANTIA UNIVERSITY, SRIGANGANAGAR**  
**III YEAR B.TECH - V SEMESTER (MAIN) EXAMINATIONS- JAN.2016**  
**TIME TABLE**

TIME - 10-30 AM TO 01.30 PM

BRANCH	DATE AND DAY					
	06-01-2016 Wednesday	11-01-2016 Monday	20-01-2016 Wednesday	23-01-2016 Saturday	27-01-2016 Wednesday	29-01-2016 Friday
<b>CIVIL ENGINEERING (01-C E)</b>	Theory of Structures-I (5CE1A)	Environmental Engineering-I (5CE2A)	Geotechnical Engineering-I (5CE3A)	Surveying-II (5CE4A)	Building Design (5CE5A)	Solid Waste Management (5CE6.3A)
<b>ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)</b>	Power Electronics (5EX1A)	Analog Communication (5EX2A)	Control Systems (5EX3A)	Data Base Management System (5EX4A)	Transmission & Distribution of Electrical Power (5EX5A)	Principle of Communication Systems (5EX6.2A)
<b>MECHANICAL ENGINEERING (03-M E)</b>	Heat Transfer (5ME1A)	Dynamics of Machines (5ME2A)	Measurement & Metrology (5ME3A)	Quality Assurance and Reliability (5ME4A)	Sociology and Economics for Engineers (5ME5A)	Computer Aided Design and Graphics (5ME6.1A)
<b>ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)</b>	Signals & Systems (5EC1A)	Linear Integrated Circuits (5EC2A)	Telecommunication Engineering (5EC3A)	Analog Communication (5EC4A)	Microwave Engineering-I (5EC5A)	Advanced Data Structures (5EC6.2A)
<b>COMPUTER SCIENCE AND ENGINEERING (05-C S E)</b>	Computer Architecture (5CS1A)	Digital Logic Design (5CS2A)	Telecommunication Fundamentals (5CS3A)	Database Management Systems (5CS4A)	Operating Systems (5CS5A)	Advanced Data Structure (5CS6.1A)